

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD - 7**

Examination Centre
Main Building
UCE (A), O.U.

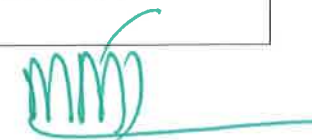
**M. E. (ECE, Mechanical & CSE) II - Semesters (Make-up) Examination
September/October 2018**

M V S R ENGINEERING COLLEGE

REVISED EXAMINATION TIME-TABLE

Time : 2.00 PM to 5.00 PM

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	II-Semester	II-Semester	II-Semester
19-09-2018 WEDNESDAY	VLSI Physical Design	Flexible Manufacturing Systems	Data Mining
22-09-2018 SATURDAY	Field Programmable Gate Arrays	Automation	Advanced Databases
24-09-2018 MONDAY	Low Power VLSI Design	Additive Manufacturing Technologies & Applications	=====
26-09-2018 WEDNESDAY	Real Time Operating Systems	Vibration Analysis & Condition Monitoring	Distributed Computing
28-09-2018 FRIDAY	Digital IC Design	=====	Soft Computing
01-10-2018 MONDAY	=====	Computer Aided Modelling & Design	Software Project Management
05-10-2018 WEDNESDAY	Digital System Design	=====	Network Security
08-10-2018 FRIDAY	=====	Computer Aided Mechanical Design & Analysis	=====



**DIRECTOR OF EVALUATION
EXAMINATION CELL**